

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|---|--------------------|------------------|---------|------------------|
| L1 | 5267 | stor\$3 near2 mask\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:47 |
| L2 | 870 | register\$1 near3 1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:48 |
| L3 | 59 | instruction\$1 with 2 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:48 |
| L4 | 11 | instruction\$1 with (address\$2 adj2 mask\$3 adj register\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:50 |
| L5 | 169 | address\$2 adj2 mask\$3 adj register\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:50 |
| L6 | 3369 | read adj modify adj write | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:50 |
| L7 | 343 | (privilege\$1 or kernel) adj3 register\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:51 |
| L8 | 0 | 6 with 7 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:51 |
| L9 | 32 | 6 and 7 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:52 |
| L10 | 6 | mask\$3 with 7 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:52 |
| L11 | 63 | (supervisor\$1) adj register\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:55 |
| L12 | 219 | (supervisor\$1) adj3 register\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:56 |
| L13 | 0 | 6 with 12 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:56 |
| L14 | 0 | psw with 6 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:57 |
| L15 | 20 | psw with modify | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:57 |
| L16 | 0 | 6 with psw | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:57 |
| L17 | 64 | 6 with exception\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:57 |
| L18 | 269 | 712/229.ccls. | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:57 |
| L19 | 111723 | atomic\$5 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:58 |
| L20 | 1084 | instruction\$1 with 19 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 15:58 |
| L21 | 664 | interrupt\$3 and (instruction\$1 with atomic\$5) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:00 |
| L22 | 27111 | (control\$4 or privilege\$1 or status) adj register\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:01 |

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|-----|------|--|--------------------|----|-----|------------------|
| L23 | 302 | 21 and 22 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:01 |
| L25 | 3369 | read adj modify adj write | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:01 |
| L26 | 49 | 22 with 25 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:02 |
| L27 | 5 | opcode with 25 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:02 |
| L28 | 126 | field\$1 with 25 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:03 |
| L29 | 125 | address\$3 and 28 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:03 |
| L30 | 12 | address\$3 with 28 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:03 |
| L31 | 139 | instruction\$1 with (clear\$3 adj 2 interrupt adj mask\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:04 |
| L32 | 1659 | (clear\$3 adj 2 interrupt adj mask\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:05 |
| L33 | 41 | destination with 25 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:05 |
| L34 | 24 | mask\$3 near3 (general adj purpose adj register\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:05 |
| L35 | 1055 | kernel adj mode | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:06 |
| L36 | 1 | (mask\$3 near3 (general adj purpose adj register\$1)) and (kernel adj mode) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:07 |
| L37 | 819 | (clear\$3 or reset\$5) near2 interrupt near3 bit\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:08 |
| L38 | 430 | (clear\$3 or reset\$5) near2 interrupt adj2 bit\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:08 |
| L39 | 1 | (privilege\$1 or supervisor\$1 or kernel) near3 ((clear\$3 or reset\$5) near2 interrupt adj2 bit\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:11 |
| L40 | 1 | (privilege\$1 or supervisor\$1 or kernel) with ((clear\$3 or reset\$5) near2 interrupt adj2 bit\$1) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:11 |
| L41 | 1981 | (privilege\$1 or supervisor\$1 or kernel) adj mode | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:12 |
| L42 | 114 | 37 and 41 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:12 |
| L43 | 74 | 38 and 41 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:12 |
| L44 | 0 | kernal adj mode adj mask\$3 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:13 |

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| L45 | 1 | kernel adj mode adj mask\$3 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:13 |
| L46 | 41 | "5210874".uref. | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:14 |
| L47 | 3509 | (status adj register) near3 (updat\$3 or clear\$1 or set\$4) | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:15 |
| L48 | 47 | mask\$3 with 47 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:15 |
| L49 | 6 | kernel adj mode adj instruction\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:15 |
| L50 | 1 | privelege\$1 adj instruction\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:16 |
| L51 | 568 | privilege\$1 adj instruction\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:16 |
| L52 | 1843 | register adj field\$1 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 16:16 |
| L53 | 86 | 51 and 52 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 17:03 |
| L54 | 140 | 712/224.ccls. | US-PGPUB; USPAT | OR | OFF | 2005/03/10 17:03 |
| L55 | 10 | (kernel or priviledge\$1) and 54 | US-PGPUB; USPAT | OR | OFF | 2005/03/10 17:04 |